

Search Notes



Application/Control No.

10/624,614

Examiner

Patrick J. Assouad

Applicant(s)/Patent under Reexamination

LU, ZIYANG

Art Unit

2857

SEARCHED

Class	Subclass	Date	Examiner
702	117 118 119 120	12/15/04	POD
↓	122 123		
703	13 14 15 16 19		
↓			
714	28 30 724 731 738 741 744		
↓			
716	4 6		

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner
702	123	12/15/04	POD
703	19		
714	741 744		
11	731		
716	6		

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	DATE	EXMR
PLUJ Report reviewed & attached.	12/15/04	POD
IEEE & E&T searched. See attached.	12/15/04	POD
Keywords: multiple time (clock) domains logic simulator		
tester (synchronous and asynchronous) virtual tester		